UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/719,342	11/21/2003	Valdimir Sklovsky	CS22954RA/10-192	3315

51874	7590	07/31/2006

LAW OFFICES OF CHARLES W. BETHARDS, LLP
P.O. BOX 1622
COLLEYVILLE, TX 76034

EXAMINER
PATEL, NITIN

ART UNIT	PAPER NUMBER
2629	

DATE MAILED: 07/31/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>3</u> MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) ☒ Responsive to communication(s) filed on *27 April 2006*.

2a) ☐ This action is **FINAL**.   2b) ☒ This action is non-final.

3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) ☒ Claim(s) *1-22* is/are pending in the application.

     4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) ☐ Claim(s) _____ is/are allowed.

6) ☒ Claim(s) *1-15,17 and 20-22* is/are rejected.

7) ☒ Claim(s) *16,18-19* is/are objected to.

8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) ☐ The specification is objected to by the Examiner.

10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.

     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

     a) ☐ All   b) ☐ Some *   c) ☐ None of:

         1. ☐ Certified copies of the priority documents have been received.

         2. ☐ Certified copies of the priority documents have been received in Application No. _____.

         3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

     * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)

2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
     Paper No(s)/Mail Date _____.

4) ☐ Interview Summary (PTO-413)
     Paper No(s)/Mail Date _____.

5) ☐ Notice of Informal Patent Application (PTO-152)

6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1.   The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that

form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2.   Claims 1-15,17,20-22 are rejected under 35 U.S.C. 102(e) as being anticipated

by Ben-Chorin et al., (US 20060050079).

As per claim 1, Ben-Chorin shows an electronic device (element 503 in fig.5)

comprising: a display panel; a display controller (element 505 in which a cpu includes in

section 0071) that controls the display panel; a plurality of data lines that supply image

data to the display panel, wherein the data lines include data lines for supplying red

image data, data lines for supplying green image data, and data lines for supplying blue

image data (in section 0071 and in fig.1 R, G, B signals); and masking gates(read as

logic circuit in element 505) that mask selected ones of the data lines at predetermined

times to adjust the pixel depth of the image data supplied to the display panel(in section

0011,0027,0028,0036,0049).

As per claim 2, Ben-Chorin shows the electronic device includes a processor (in

section 0071) coupled to the masking gates, and the processor provides one or more

control signals to an input of each of the masking gates.

As per claim 3, Ben-Chorin shows the electronic device includes a processor (in section 0071), wherein the processor is coupled to the masking gates, and the processor causes the masking gates to mask the selected ones of the data lines when the electronic device is in a power saving mode.

As per claim 4-6 Ben-Chorin shows masking gates are coupled to an input of the display controller and comprising a display panel interface, wherein an output of the display panel interface is coupled to an input of one or more of the masking gates and further comprising a video buffer, wherein an output of the video buffer is coupled to an input of one or more of the masking gates (In fig.5 and section 0071).

As per claim 7,8,15,21 Ben-Chorin does not specifically teach the electronic device is a mobile telephone and the masking gates include an AND gate. Examiner is taking an official noticed that it is well known in the art to have a display with LCD as taught by Ben-Chorin would be used in mobile device and any other devices that have a display with LCD display and also a logic gate could be any of the gates which includes an AND, OR, NAND gates are well known in the art.

AS per claims 9,17 Ben-Chorin shows a method of reducing power consumption of an electronic device by reducing the pixel depth (in section 0011-0016 and 0023 to use to adjusted the intensity of the pixel for power consumption) of image data transmitted to a display panel, wherein the method comprises masking selected data lines that transmit image data to the display panel at certain times (in fig.5).

As per claims 10,12 Ben-Chorin shows the method comprises masking selected data lines that supply red image data, selected lines that supply green image data, and

selected lines that supply blue image data such that the selected data lines maintain a constant level when the electronic device is in a power saving mode (in fig.1 and in section 0007-0011).

As per claims 11, 13,14 Ben –Chorin teaches data lines being masked are a predetermined number of the least significant bits associated with each of the lines supplying red image data, green image data, and blue image data (In fig.1).

As per claim 20, Ben-Chroin teaches masking of selected data lines includes masking selected data lines coupled to the input of a display controller (in fig.5).

As per claim 22, ben-Chroin shows employing a processor to determine (in section 0071) which of a plurality of data lines are the selected lines, wherein the method further includes varying the number of selected lines to vary the power consumption of the electronic device.

Allowable Subject Matter

3.   Claims 16,18,19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The prior art fails to teach or suggest masking reduces the pixel depth of the image data, and the method includes producing images based on a full pixel depth, when masking is not being performed, and producing images at a reduced pixel depth, when the masking is being performed, and the method includes using the same image data as a source when producing images based on a full pixel depth and when producing images at a reduced pixel depth as claimed in claim 16.

The prior art fails to teach or suggest the method comprises disabling the masking when the electronic device is not in a power saving mode as claimed in claim 18.

The prior art fails to teach or suggest the method includes permitting data lines that are not masked to change state according to image data being sent to the masking gates as claimed in claim 19.

Response to Arguments

4.   Applicant's arguments with respect to claims 1-22 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

5.   Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Patel whose telephone number is 571-272-7677. The examiner can normally be reached on 8:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin H. Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a

USPTO Customer Service Representative or access to the automated information

system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Nitin Patel
Examiner
Art Unit 2629